

## 1 Homework - Due Sept 18

1. Download the FullAdder.lgi Multimedia Logic file from the course web site onto a Windows machine with this software (such as the UAAlbany public PCs). Run Multimedia Logic on this file. Edit all text boxes with .... in them so the dots are replaced by the Boolean expression that represents the calculation the circuit did on A, B and C to generate the signal which that box labels. (Use the select tool, left-click and activate the Properties... dialog. The simulation must be stopped for editing, saving, etc. to work.)
2. "Build" the circuits given in PP's Figures 3.12, 3.13, 3.18, and 3.19 inside on Multimedia Logic .lgi file. Drive the inputs from switches, and put LEDs on the outputs. Reproduce the material presented in lecture so you really understand the difference between a combinational and a sequential circuit.

Submit the two resulting .lgi files to this assignment on WebCT.

3. PP's exercises 3.23, 3.25, and 3.27

## 2 Quiz: Rip off here and hand in.

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Demonstrate the 2-stacks algorithm as it evaluates the boolean expression:

0 & ( ~ 0 | 1 ) | 0

Show the pop operation on each stack by crossing out the popped element. Show the push operation by writing the new element on top of the stack. When you push something on top of a stack where an entry had been crossed out, you must REDRAW the stack without the crossed out elements.

How do UA students submit assignments on WebCT?

Name \_\_\_\_\_